





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: William R. Wheeler et al.

Serial No.: 09/941,158

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: August 28, 2001

Title

MODEL-BASED LOGIC DESIGN

Commissioner for Patents Washington, D.C. 20231

RESPONSE TO RESTRICTION REQUIREMENT

Responsive to the action mailed December 3, 2002, applicant elects the invention of Group I drawn to the embodiment of methods and system for designing a logic circuit comprising maintaining a data structure. The election is made without traverse.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Art Unit: 2812

Examiner: Stacy Whitmore

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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